

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

EXPRESS MAIL NO. EV 327878912 US

Applicant : Ian S. Eslick et al.
Application No. : To be Assigned
Filed : Filed Herewith
Title : SYSTEM AND METHOD FOR EXECUTING HYBRIDIZED CODE ON
A DYNAMICALLY CONFIGURABLE HARDWARE ENVIRONMENT
Grp./Div. : To be assigned
Examiner : To be Assigned
Docket No. : 51346/PAN/B600

**INFORMATION DISCLOSURE STATEMENT
FOR A CONTINUING NON-CPA APPLICATION**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PostOffice Box 7068
Pasadena, CA 91109-7068
October 27, 2003

Commissioner:

In compliance with the duty of disclosure under 37 CFR §§ 1.56, 1.97 and 1.98, enclosed is FORM PTO/SB/08A/B. The references listed thereon are not enclosed because they were of record in a parent application, Application No. 09/608,860, filed June 30, 2000.

It is respectfully requested that these references be considered in the examination of this application, and identified in the list of references cited on the patent issuing on this application.

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

By 
Peter A. Nichols
Reg. No. 47,822
626/795-9900

PAN/cks
Enclosure(s)
CKS PAS533258.1-* 10/27/03 9:28 AM

FORM PTO/SB/08A/B (10-01) Substitute for PTO-1449A/B	Attorney Docket Number	51346/PAN/B600
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)	Application Number	To be Assigned
	Filing Date	Filed Herewith
	Applicant(s)	Ian S. Eslick et al.
	Group Art Unit	To be Assigned
	Examiner Name	To be Assigned

U.S. PATENT DOCUMENTS				
EXAMINER INITIALS	Cite No. ¹	DOCUMENT NUMBER Number - Kind Code ² (If Known)	Publication Date MM-DD-YYYY	Name of Patentee
		4,967,340	10-30-1990	Dawes
		5,388,215	02-07-1995	Baker et al.
		5,742,180	04-21-1998	DeHon et al.
		5,778,226	07-07-1998	Adams et al.
		5,915,123	06-22-1999	Mirsky et al.
		5,956,518	09-21-1999	DeHon et al.
		6,075,939	06-13-2000	Bunnell et al.
		6,108,760	08-22-2000	Mirsky et al.
		6,122,719	09-19-2000	Mirsky et al.
		6,351,846	02-26-2002	Collin et al.

FOREIGN PATENT DOCUMENTS					
EXAMINER INITIALS	Cite No. ¹	FOREIGN PATENT DOCUMENT Country Code ³ - Number ⁴ - Kind Code ⁵ (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ⁶ (<input checked="" type="checkbox"/>)

OTHER DOCUMENTS		
EXAMINER INITIALS	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		"Smart Compilers Puncture Code Bloat," Brown, Electronic Engineering Times, October 9, 1995 (pages 38 & 42)
		"A High-Performance Microarchitecture with Hardware-Programmable Functional Units," Razdan et al., Micro-27 Proceedings of the 27th Annual International Symposium on Microarchitecture, 11/30 - 12/2/94 (pp. 172-180).
		"Programmable Active Memories: Reconfigurable Systems Come of Age," IEEE Transactions on VLSI Systems, 1995 (pp. 1-15).
		"Pilkington Preps Reconfigurable Video DSP," Clark, EE Times, week of July 31, 1995

EXAMINER SIGNATURE		DATE CONSIDERED	
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.pto.gov or MPEP 901.4. ³ Enter Office that issued the document, by the two-letter code (WIPO standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English Language Translation is attached.			

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		"Coarse-Grain Reconfigurable Computing," Mirsky, Ethan A., Thesis submitted at the Massachusetts Institute of Technology, June 1996
		"SOP: Adaptive Massively Parallel System," by Tsukasa Yamauchi et al., NEC Research & Development, Vol. 37, No. 3, July 1996 (pp. 382-393).

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